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wherein the input power is transmitted along a path constituting of a first terminal from which the input power is inputted, the active bias circuit, the power amplified transistor, a terminal from which the gate bias voltage is outputted in that sequence.

Claim 14 (original) The method of claim 13, wherein a curve of an increase of the gate bias voltage versus the input power is a linear curve.

Claim 15 (original) The method of claim 13, wherein a curve of an increases of the gate bias voltage versus the input power is a non-linear curve.

Claim 16 (previously presented) The power amplifier of claim 1, wherein the active bias circuit comprises a grounded level.

Claim 17 (previously presented) The method of claim 7, wherein the active bias circuit comprises a grounded level.